



# Notice of Allowability

Application No.

10/671,561

Examiner

John H. Le

Applicant(s)

LARSSON ET AL

Art Unit

2863

The MAILING DATE of this communication appears on the cover sheet with the correspondence address--  
All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included  
herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS  
NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative  
of this Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 10/23/2006.
2. ☒ The allowed claim(s) is/are 1-12.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All b) ☐ Some\* c) ☐ None of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

***Examiner's Amendment***

***Response to Amendment***

1. Applicant's amendment filed 10/23/2006 and 10/16/2006 has been entered and carefully considered.

Claims 1, 4, and 9 have been amended.

Claims 13-14 have been canceled.

The specification has been amended.

***Examiner's Amendment***

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Attorney Martin E. Miller on November 8, 2006.

The applicant has been amended as follows:

1. (Currently Amended) A computer-implemented method for determining parameters of an equivalent circuit representing a transmission section of an electrical network, where the transmission section is representable as having at least two interfaces with other sections of the network, wherein the method comprises the steps of:
  - a) measuring, by means of at least two synchronized phasor measurement units residing at each of the at least two interfaces, a voltage phasor at the interface and

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a phasor of a current flowing through the interface, measurements at the different interfaces being made essentially simultaneously,

b) computing, from said voltage and current phasors, values of impedances constituting the equivalent circuit; and

(c) displaying changes in the equivalent circuit based on the computed values of impedances.

9. (Currently Amended) A computer program embodied on a computer readable medium for determining parameters of an equivalent circuit representing a transmission section of an electrical network having at least two interfaces with other sections of the network, which computer program is loadable and executable on, a data processing unit and which computer program, when being executed, performs the steps of computing, from essentially simultaneous measurements at each of the different interfaces of a voltage phasor at the interface and a phasor of a current flowing through the interface, values of impedances constituting the equivalent circuit, and displaying changes in the equivalent circuit based on the computed values of impedances.

***Reasons for Allowance***

2. Claims 1-12 are allowed.

The following is a statement of reasons for the indication of allowable subject matter:

Please see the previous office action and applicant's argument filed on 10/16/2006, 05/01/2006.

Regarding claim 1, in combination with other limitations of the claims, none of the prior art of record teaches or suggests the combination of a computer-implemented method for determining parameters of an equivalent circuit representing a transmission section of an electrical network, where the transmission section is representable as having at least two interfaces with other sections of the network, wherein the method comprises the steps of measuring, by means of at least two synchronized phasor measurement units residing at each of the at least two interfaces, a voltage phasor at the interface and a phasor of a current flowing through the interface, measurements at the different interfaces being made essentially simultaneously and computing, from said voltage and current phasors, values of impedances constituting the equivalent circuit. It is these limitations as they are claimed in the combination with other limitations of claim, which have not been found, taught or suggested in the prior art of record, that make these claims allowable over the prior art.

Regarding claim 9, in combination with other limitations of the claims, none of the prior art of record teaches or suggests the combination of a computer program embodied on a computer readable medium for determining parameters of an equivalent circuit representing a transmission section of an electrical network having at least two interfaces with other sections of the network, which computer program when being executed, performs the steps of computing, from essentially simultaneous measurements at each of the different interfaces of a voltage phasor at the interface and a phasor of a current flowing through the interface, values of impedances constituting the equivalent circuit. It is these limitations as they are claimed in the

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combination with other limitations of claim, which have not been found, taught or suggested in the prior art of record, that make these claims allowable over the prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Contact Information***

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to John H. Le whose telephone number is 571 272 2275. The examiner can normally be reached on 9:00 - 5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John E. Barlow can be reached on 571 272 2269. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

John H. Le  
Patent Examiner-Group 2863  
November 8, 2006

BRYAN BUI  
PRIMARY EXAMINER

